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OFFICIAL

PATENT

Practitioner's Docket No. 00CXT0490N-3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Wilson P. Snyder II

Application No.: 09/639,915

Group No.: 2666

Filed: 08/16/2000

Examiner: Robert C Scheibel

For: Integrated Circuit that Processes Communication Packets with Scheduler Circuitry
that Executes Scheduling Algorithms based on Cached Scheduling Parameters

Mail Stop: Non-Fee Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Response to Office Action

This is in response to the Office Action dated December 31, 2003.

Amendment to the Specification

For the paragraph that starts on page 21, line 5 please substitute the following:

AX FIG. 6 ~~FIG. 5~~ is a table that illustrates buffer classes in an example of the invention. In the example of ~~FIG. 3~~ FIG. 6, there are 16 classes with 500 external buffers each for a total of 8,000 external buffers. Each class has a type: static, exclusive, or shared. Static classes use their own external buffers without sharing. Exclusive classes use their own external buffers first, and then borrow external buffers from the fail-over classes. De-allocated external buffers from a given exclusive class are credited to the associated fail-over class until the number of borrowed external buffers is zero. Shared classes use their own external buffers and comprise the fail-over external buffers for the exclusive class.